

FIG. 1

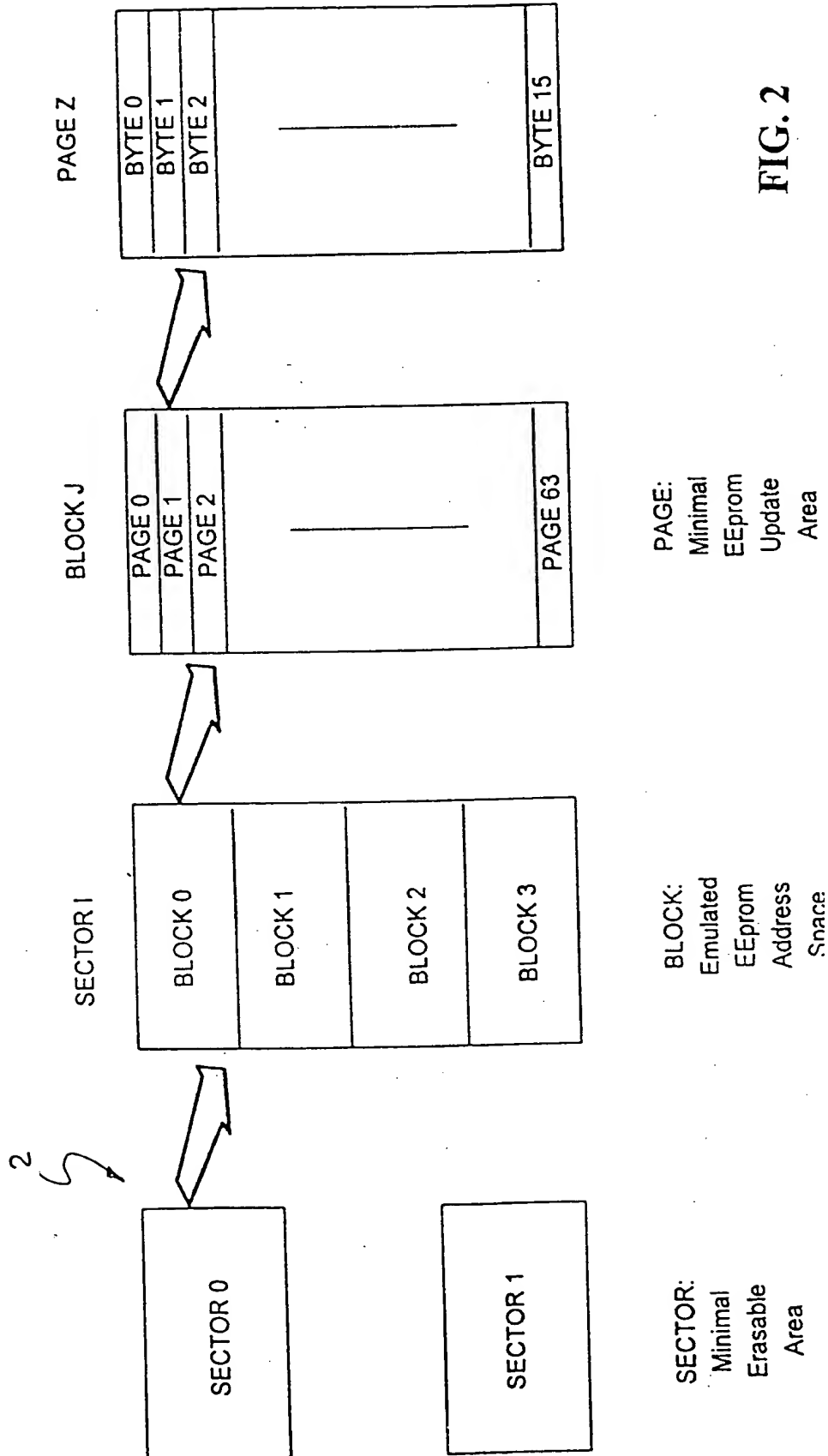


FIG. 2

Simplification: 4 pages (instead of 64) for each block

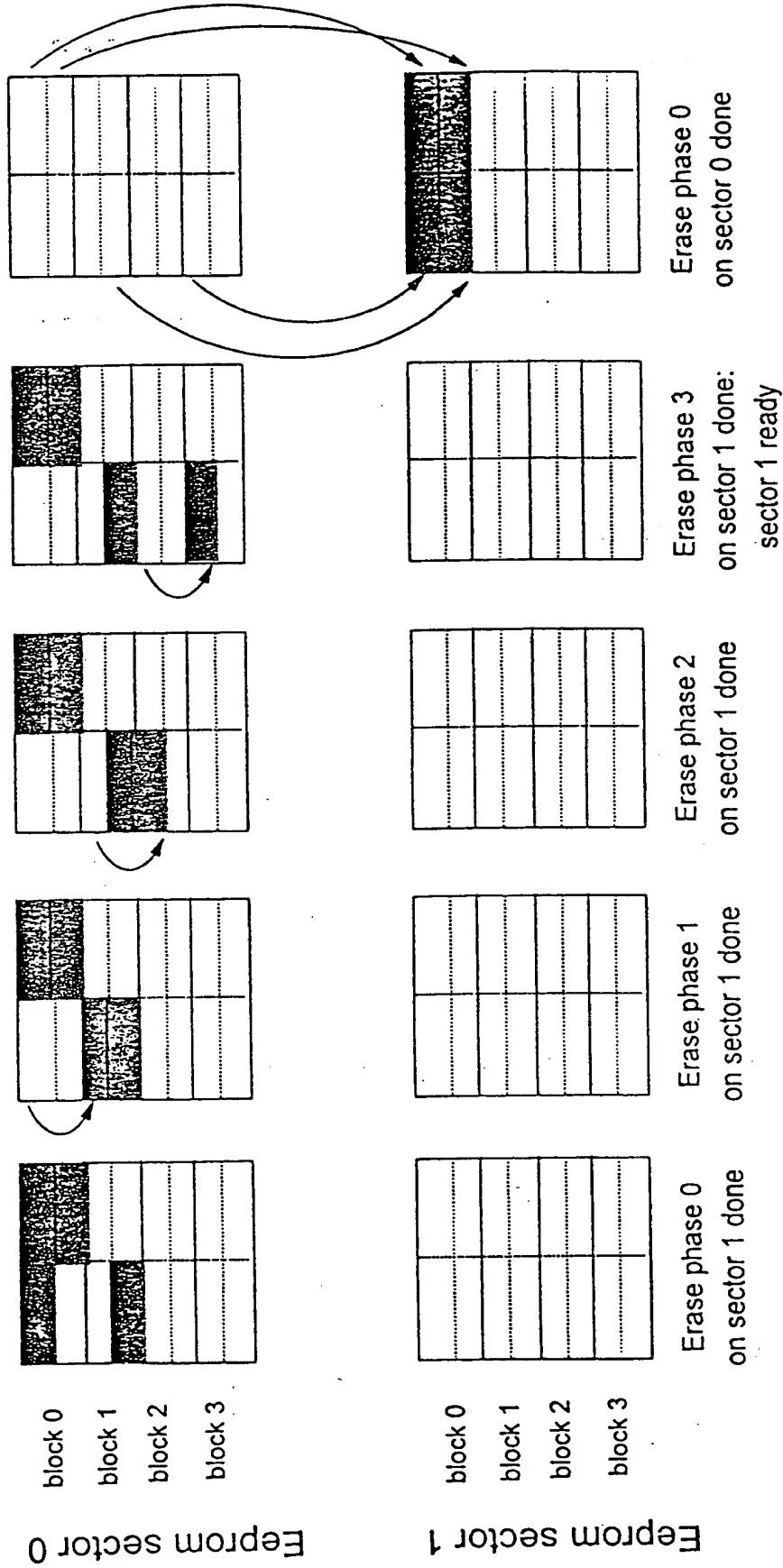


FIG. 3

**FIG. 3A**

**FIG. 3B**

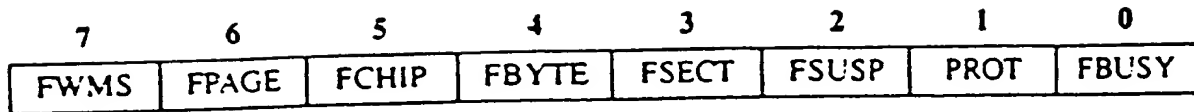


FIG. 3C

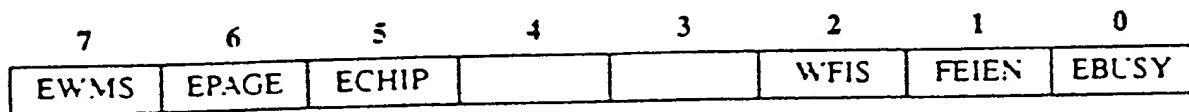


FIG. 3D

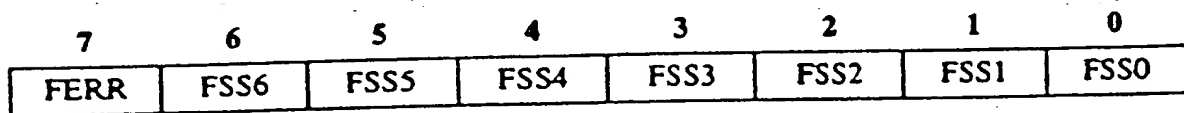


FIG. 3E

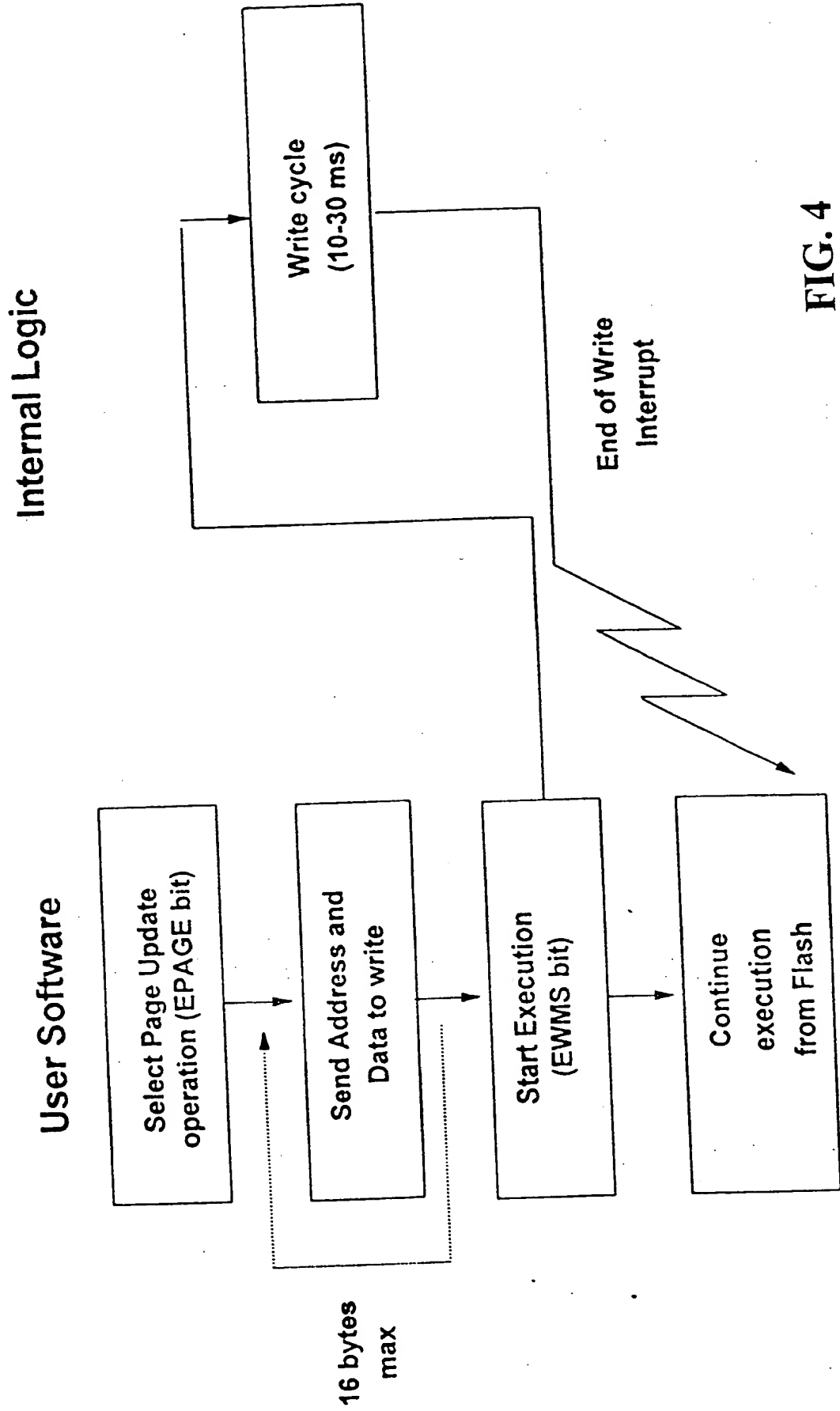


FIG. 4

211FFCh	NVAPR
211FFDh	NVWPR
211FFEh	NVPWDO
211FFFh	NVPWDI

FIG. 4A

Operation	Size	Min	Typ	Max
Page Update	256 byte	160 us	10 ms	30 ms
	512 byte	160 us	15 ms	50 ms
	1 Kbyte	160 us	30 ms	100 ms
Chip Erase	256 byte		35 ms	100 ms
	512 byte		45 ms	150 ms
	1 Kbyte		70 ms	300 ms

FIG. 4B

**FIG. 4C**

FIG. 4D

**FIG. 4E**

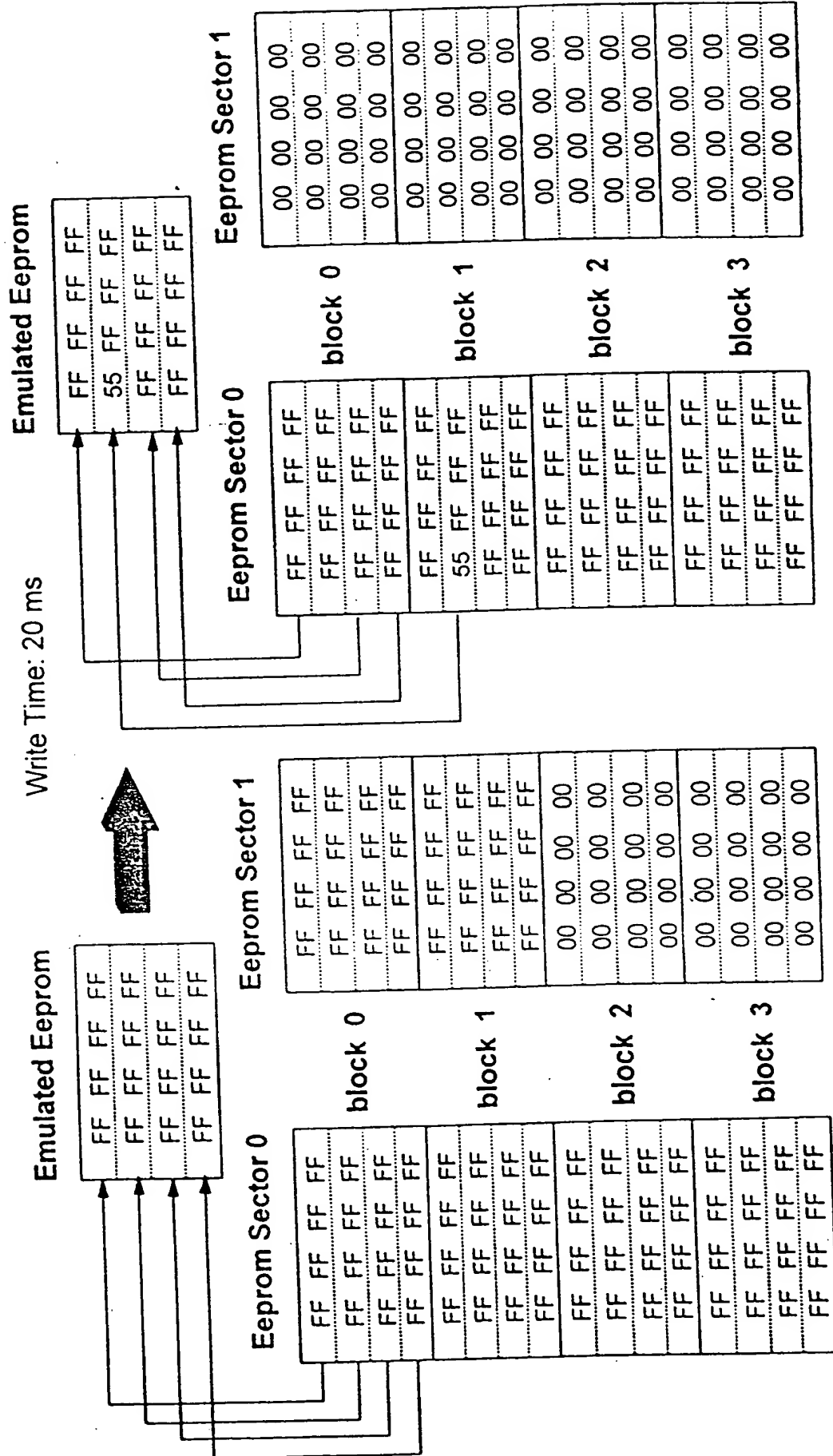
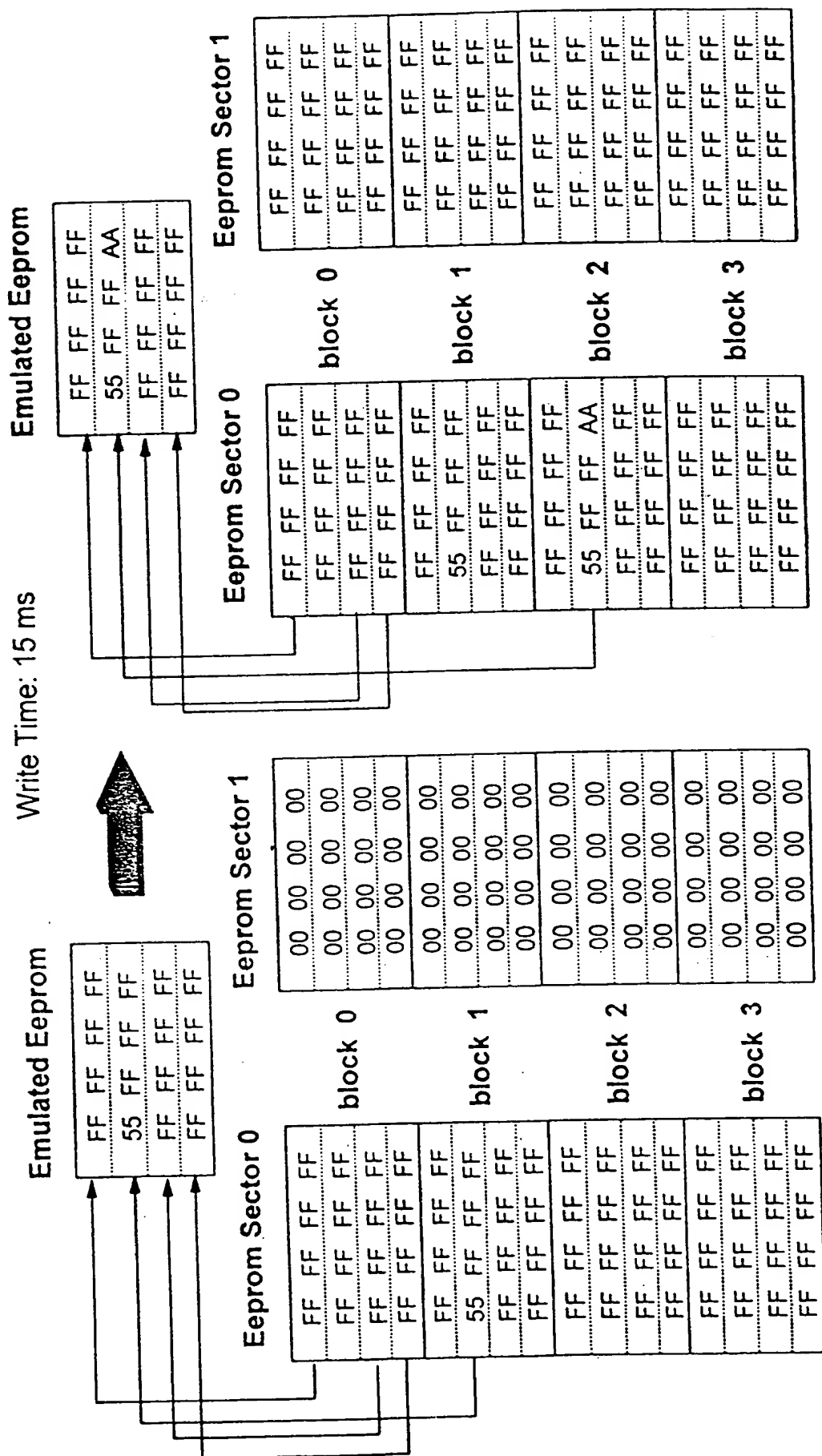


FIG. 5



**FIG. 6**



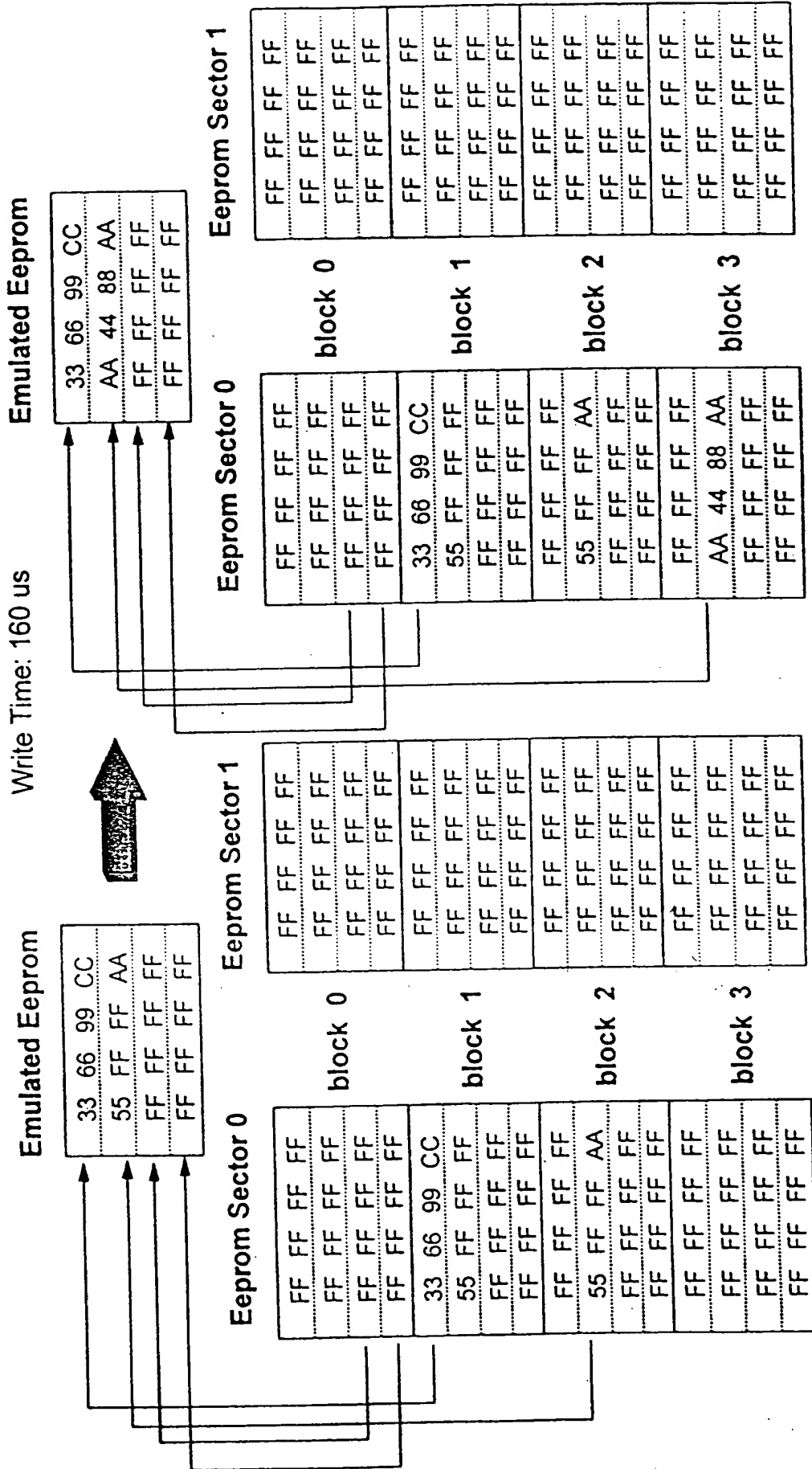


FIG. 8

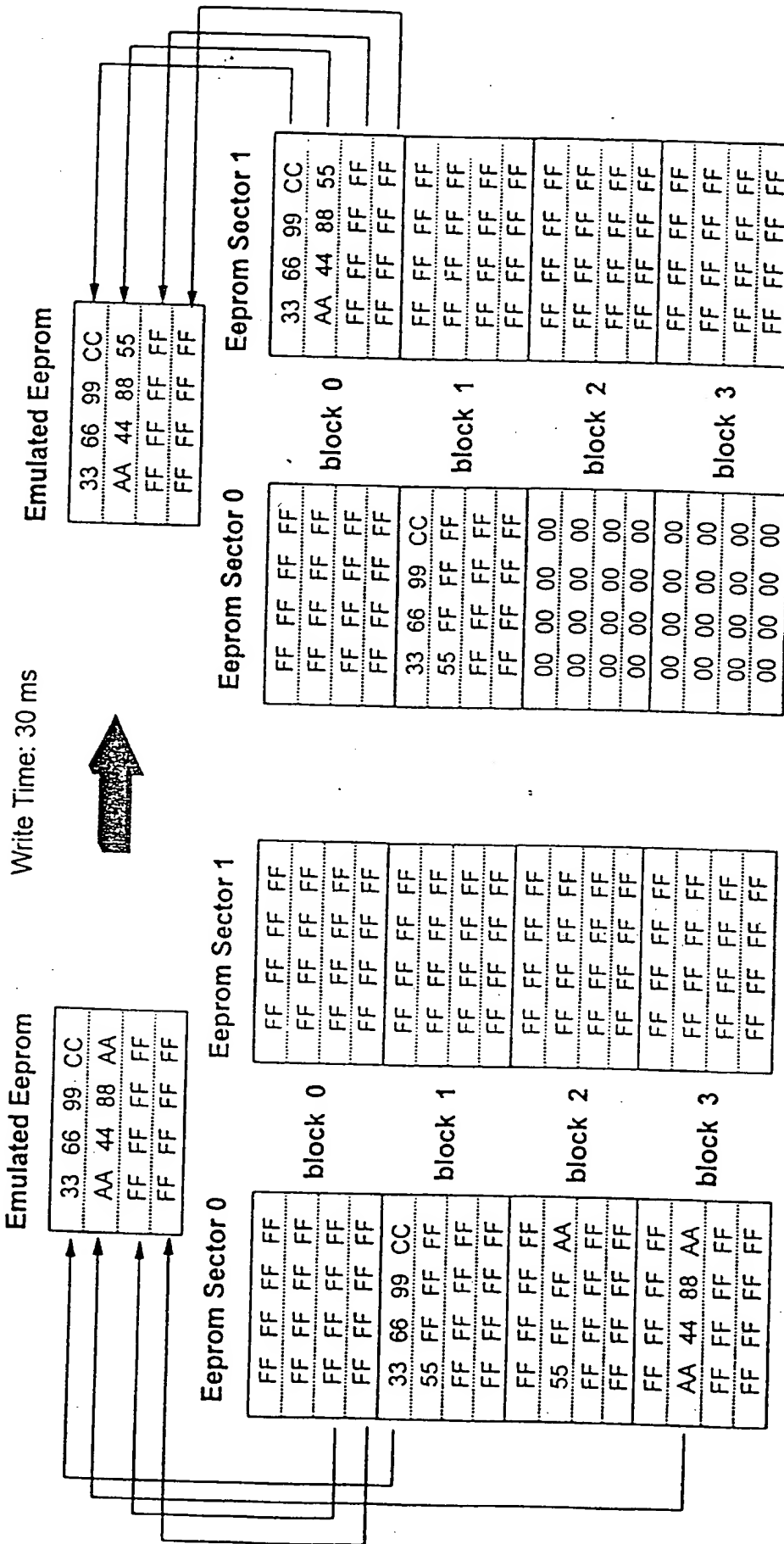
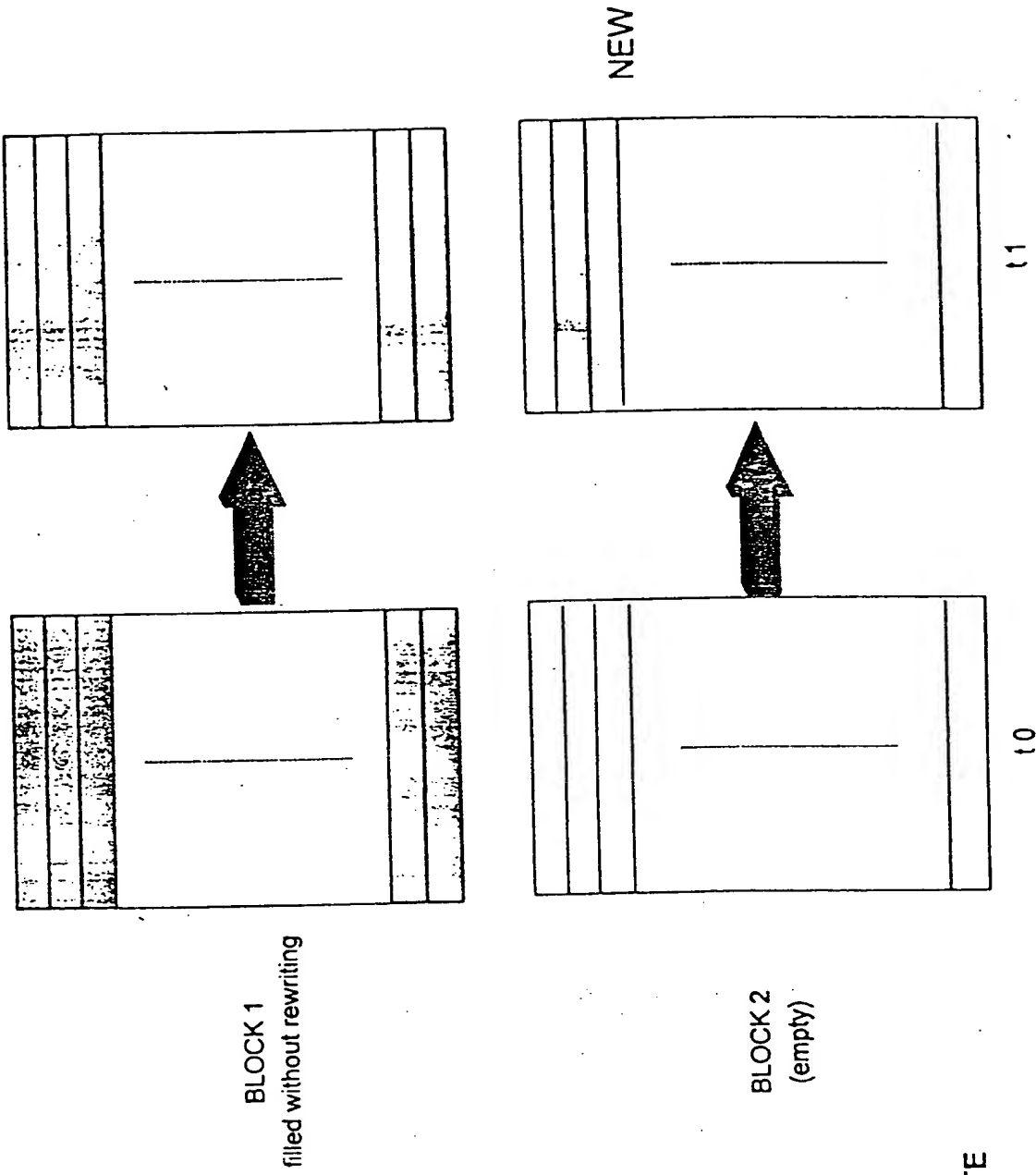


FIG. 9



CYCLES per BYTE  
100 K \* 8

FIG. 10

66000 64525

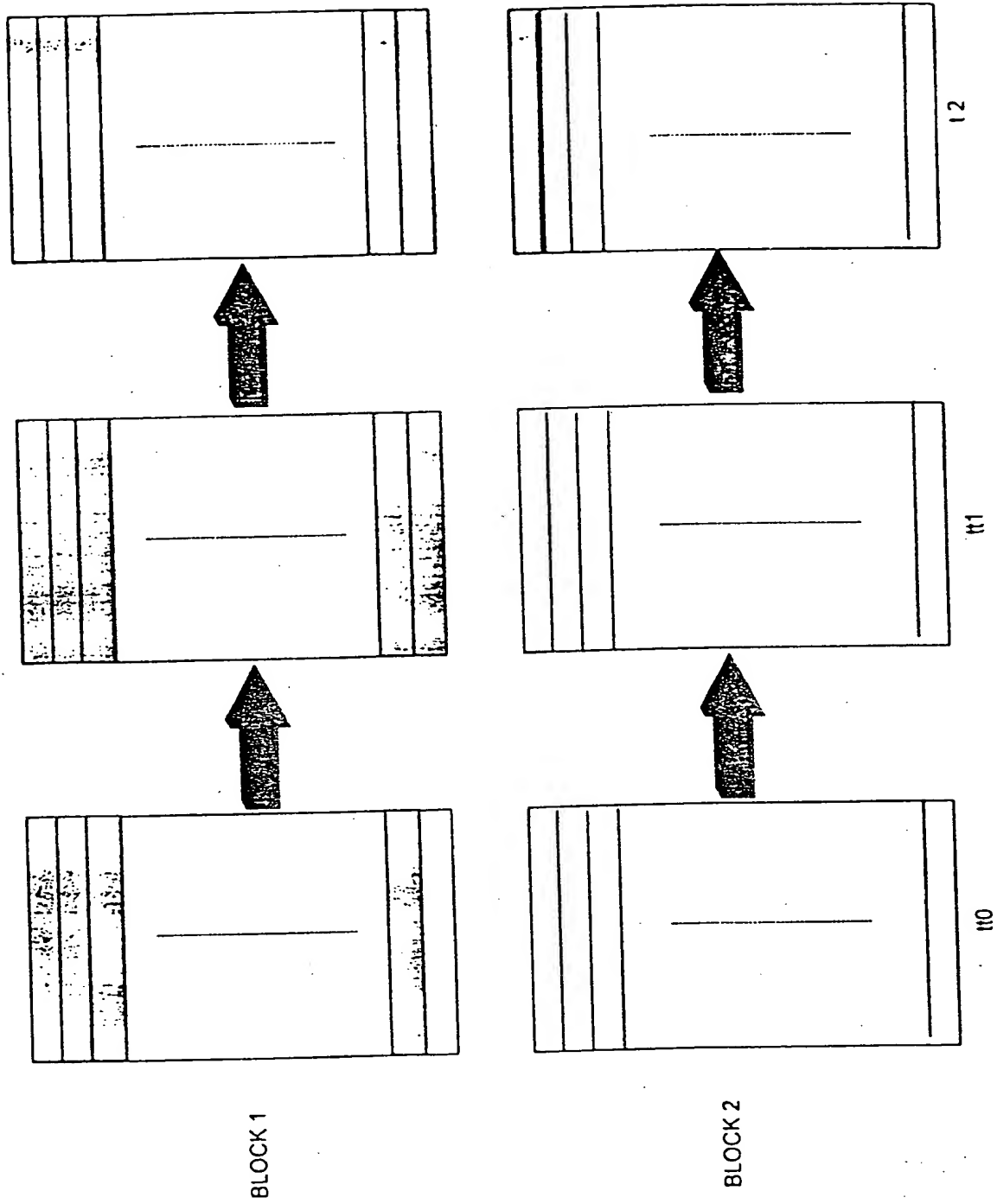
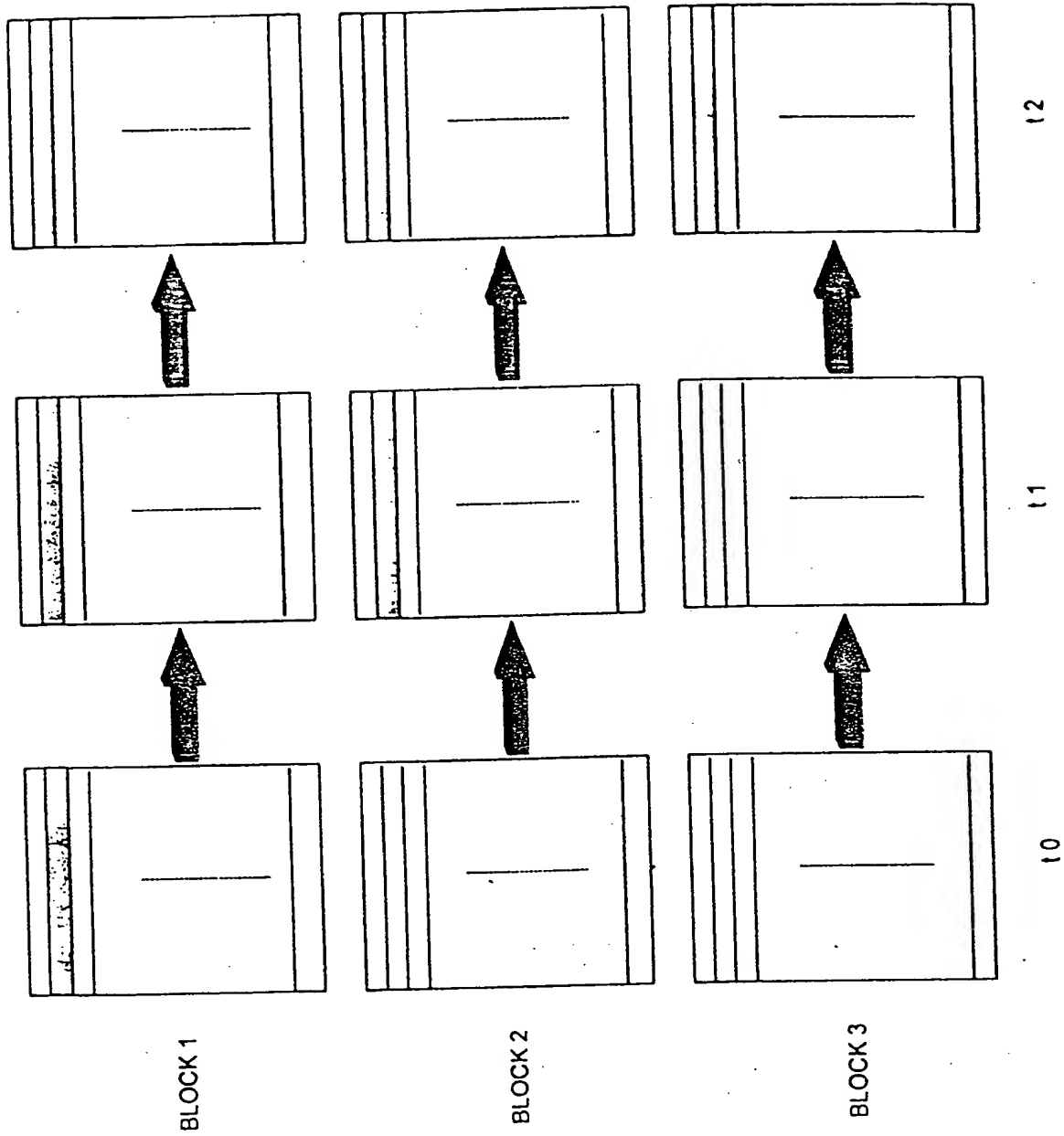


FIG. 11

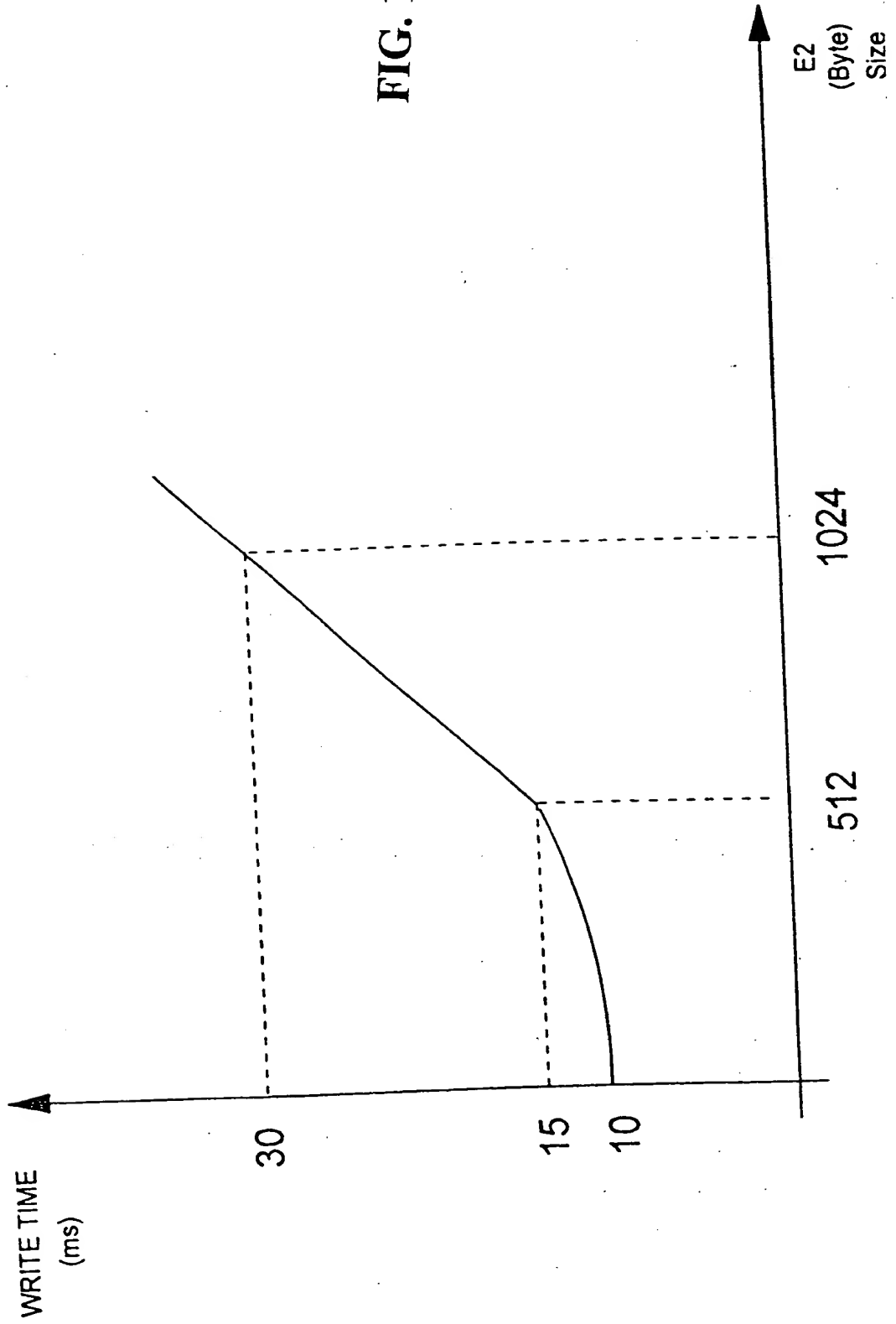
CYCLES per BYTE  
 $100K \cdot 8 / 1K \cdot 64 = 51200$



CYCLES per BYTE  
 $100K \cdot 8 / 1K = 800$

FIG. 12

FIG. 13



14.

